# BIOGRAPHY



I am **Dr. T S Arulananth** working as an Associate Professor in the Department of Electronics and Communication Engineering at MLR Institute of Technology (Autonomous Institute), Hyderabad. I have completed my B.E in 2004, M.Tech in 2007 and finished my Ph.D in 2016 in the area of Image Processing.

My total teaching experience is 17 years in the field of Electronics and Communication Engineering. Presently, I have been guiding 3 - research scholars under Saveetha University and SRM University, Chennai. I have also guided many students' projects of both UG and PG programme.

I have organized 24 events like seminar, workshop, conference, FDP and video conferencing. I have received three awards namely Bharath Vikas Award, I20R Award, IGIP International Engineering Educator Awards.

I have attended 76 events like seminar, workshop, conference, FDP, webinar and video conferencing. I served as an Editorial board member for 4 journals. I am in the member of IEEE, ISTE, SDIWC, IACSIT, Internet Society, IFERP, IAENG, IRED, ISRD, European Association for Signal Processing.

I have published 46 Journals, presented 22 papers in International Conferences and 02 papers in National Conferences.

I have completed one DST funding project (Department of Science and Technology) for my college.

I have published a patent in government of India, published three books and three book chapters.